

FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)

Date Submitted to PTO: October 15, 2002

ATTY DOCKET NO.
2001_1828

OCT 15 2002

SERIAL NO.
10/017,392APPLICANT
Yuusaku OHTA et al.FILING DATE
December 18, 2001GROUP
2131

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

RECEIVED
OCT 17 2002
Technology Center 2100

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
TT	AJ	00/30262	5/2000	WO			
	AK						
	AL						
	AM						
	AN						

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

TT	AO	WASSAL A.G., et al., "A VLSI ARCHITECTURE FOR ATM ALGORITHM-AGILE ENCRYPTION", Proceedings Great Lakes Symposium on VLSI, March 4, 1999, pages 325-328.
	AP	
	AQ	

EXAMINER

DATE CONSIDERED 05.05.05

*EXAMINER: Initials if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AJ							
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	AL							
	AM							
	AN							

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

II	AQ	"ADSP-2141L Safenet DSP Security System on a Chip" by Analog Devices, pp. 1-5.						
	AP							
	AQ							
EXAMINER	19/017392				DATE CONSIDERED	05.05.05		

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